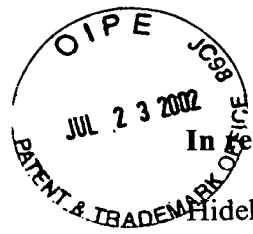


#3/Election
7/30/02
JS



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hideki Tanaka

Serial No.: 09/788,503

Group Art Unit: 2823

Filed: February 21, 2001

Examiner: Brairton, Scott A.

For: TAPE CARRIER TYPE SEMICONDUCTOR DEVICE, METHOD FOR
MANUFACTURING THE SAME, AND FLEXIBLE SUBSTRATE

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231

RECEIVED
JUL 24 2002
TECHNOLOGY CENTER 2800

**RESPONSE TO FIRST OFFICE ACTION AND RESTRICTION
REQUIREMENT**

Sir:

In response to the Office Action dated July 2, 2002, please consider the following:

REMARKS

In response to the Examiner's restriction requirement, Applicant hereby elects the invention of Group I (e.g., semiconductor device as defined by claims 1-11 and 13-18), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later.

Further, Applicant respectfully requests the Examiner's acknowledgment of the priority document filed on February 21, 2001.

Early, favorable prosecution on the merits is respectfully requested.

Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.